

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of the Claims:

A1  
Sub B1  
1. (Currently Amended) A power supply circuit for a digital processing system,  
the circuit comprising:

a first stage ~~associated with a~~ having a first output coupled to a first  
component of the digital processing system and a second output which is  
different from the first output ; and

a second stage associated with a second component of the digital  
processing system, said second stage coupled to said first stage; and

wherein said first stage drives said second stage using the second output.

2. (Original) The circuit of claim 1, wherein said first and second stages are  
separated from each other.

3. (Original) The circuit of claim 2, wherein said first and second stages are  
coupled to each other by a two wire bus.

4. (Original) The circuit of claim 3, wherein said two wire bus is differentially  
driven by said first stage.

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5. (Original) The circuit of claim 2, wherein said first component comprises a display device and said second component comprises a microprocessor.

6. (Original) The circuit of claim 5, wherein said first stage is located proximately to said display device and said second stage is located proximately to said microprocessor.

7. (Original) The circuit of claim 1, wherein said first stage provides power for said first component and said second stage provides power for said second component.

8. (Original) The circuit of claim 7, wherein said first stage comprises a flyback converter and said second stage comprises a portion of a forward converter.

9. (Currently Amended) A power supply circuit for a computer system, the circuit comprising:

a first circuit having a first output capable of providing power to a first component of the computer system and a second output which is different from the first output; and

a second circuit capable of providing power to a second component of the computer system;

wherein said first ~~and second circuits are electrically coupled to each other~~  
circuit drives the second circuit through the second output.

10. (Cancelled)

11. (Original) The circuit of claim 10, wherein said second circuit and said second component are disposed on a printed circuit board.

12. (Original) The circuit of claim 9, wherein said first circuit is located within an enclosure of the computer system and proximately to said first component, and wherein said second circuit is located within said enclosure and proximately to said second component.

13. (Original) The circuit of claim 12, wherein said first component comprises a display device and said second component comprises a microprocessor.

14. (Original) The circuit of claim 9 wherein said first circuit comprises a flyback converter and said second circuit comprises a final stage of a forward converter.

15. (Currently Amended) A computer system comprising:

a power supply circuit coupled to ~~at least~~ a display device and a microprocessor of the computer system, wherein said power supply circuit is

capable of supplying power to said display device and said microprocessor using at least two distinct power supply stages;

a main circuit coupled to said display device using a first output; and  
a secondary circuit coupled to said microprocessor; and  
wherein said main circuit drives said secondary circuit using a second output which is different from the first output .

16. (Cancelled)

17. (Original) The computer system of claim 16 wherein one of said at least two distinct power supply stages includes said main circuit, and wherein another of said at least two distinct power supply stages includes said secondary circuit.

18. (Original) The computer system of claim 16, wherein said main circuit and said secondary circuit are physically isolated from each other.

19. (Original) The computer system of claim 18, wherein said main circuit and said secondary circuit are electrically coupled to each other.

20. (Original) The computer system of claim 16, wherein said main circuit comprises a flyback converter and said secondary circuit comprises a portion of a forward converter.

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21. (New) The power supply circuit of claim 1, wherein said first output provides a direct (DC) voltage, and wherein said second output provides an alternating current (AC) voltage.

22. (New) The power supply circuit of claim 1, wherein said first output provides a direct current (DC) voltage and said second output provides an alternating current (AC) voltage.

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